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J1011 U.S. PRO
J09/971655

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Class	Subclass	ISSUE CLASSIFICATION

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PATENT NUMBER

U.S. UTILITY Patent Application

<i>F.A</i>	O.I.P.E.	PATENT DATE
SCANNED <u>AC3</u>	Q.A. <u>A.A</u>	

APPLICATION NO. 09/977655	CONT/PRIOR D	CLASS 710	SUBCLASS 317	ART UNIT 2182 2181	EXAMINER <u>Austin Dang</u>
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John Liron

TITLE

Routing switcher with variable input/output architecture

PTO-2040
12/99

ISSUING CLASSIFICATION

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____				ISSUE FEE	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER	
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